




INTEGRATED CIRCUIT COMPUTING DEVICE COMPRISING DYNAMICALLY CONFIGURABLE GATE ARRAY HAVING A RECONFIGURABLE EXECUTION MEANS

Patent number: WO9414123
Publication date: 1994-06-23
Inventor: GILSON KENT L
Applicant: NAT TECHNOLOGY INC [US]
Classification:
 - international: G06F15/20
 - european: G06F9/318; G06F15/78R; G06F15/80A1
Application number: WO1993US11964 19931209
Priority number(s): US19920989236 19921211

Also published as:

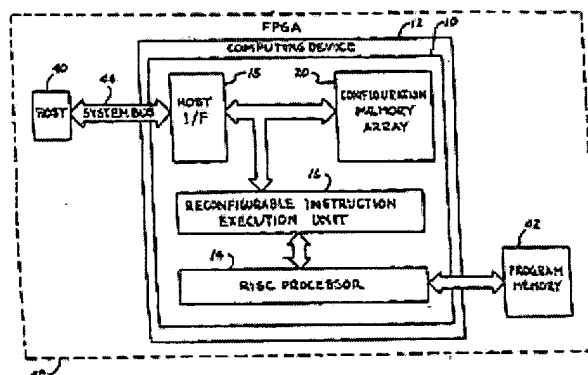
 EP0626084 (A1)
 US5361373 (A1)
 EP0626084 (A4)

Cited documents:

 US5233539
 US4870302

Abstract of WO9414123

An integrated circuit computing device (10) is comprised of a dynamically configurable Field Programmable Gate Array (FPGA) (12). This gate array (12) is configured to implement a RISC processor (14) and a Reconfigurable Instruction Execution Unit (16). Since the FPGA (12) can be dynamically reconfigured, the Reconfigurable Instruction Execution Unit (16) can be dynamically changed to implement complex operations in hardware rather than in time-consuming software routines. This feature allows the computing device (10) to operate at speeds that are orders of magnitude greater than traditional RISC or CISC counterparts. In addition, the programmability of the computing device (10) makes it very flexible and hence, ideally suited to handle a large number of very complex and different applications.



Data supplied from the **esp@cenet** database - Worldwide

PCT

WORLD INTELLECTUAL PROPERTY ORGANIZATION
International Bureau



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁵ : G06F 15/20	A1	(11) International Publication Number: WO/94/14123
		(43) International Publication Date: 23 June 1994 (23.06.94)

(21) International Application Number: PCT/US93/11964

(22) International Filing Date: 9 December 1993 (09.12.93)

(30) Priority Data:
07/989,236 11 December 1992 (11.12.92) US

(71) Applicant: NATIONAL TECHNOLOGY, INC. [US/US];
Suite 400, 2835 Hollywood Avenue, Shreveport, LA 71108 (US).

(72) Inventor: GILSON, Kent, L.; Apartment 120, 255 North Main Street, Salt Lake City, UT 84115 (US).

(74) Agent: WEISS, Harry, M.; Harry M. Weiss & Associates, 4204 North Brown Avenue, Scottsdale, AZ 85251 (US).

(81) Designated States: JP, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

Published

With international search report.

Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

(54) Title: INTEGRATED CIRCUIT COMPUTING DEVICE COMPRISING DYNAMICALLY CONFIGURABLE GATE ARRAY HAVING A RECONFIGURABLE EXECUTION MEANS

